# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 632 | (block with model) and (dynamic with (fifo or memory)) and simulat$5 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:30 |
| L2 | 51 | 1 and size and (wait adj (period or time)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:33 |
| L3 | 49 | (simulation with backplane) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:33 |
| L4 | 13 | 3 and fifo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:41 |
| L5 | 0 | (module adj level adj interface) and fifo and token | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:42 |
| L6 | 1217 | (module with interface) and fifo and token | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:42 |
| L7 | 487 | 6 and simulat$5 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:42 |
| L8 | 126 | 7 and (verilog or vhdl) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:43 |

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| L9 | 126 | 8 and timing | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:59 |
| L10 | 15 | (blocks with behavior) same fifo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 11:59 |
| L11 | 4 | ("5544067" \| "5933356" \| "6233540" \| "6324678").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/12/19 12:11 |
| L12 | 2130 | ((703/22,15,19) or (716/1)).CCLS. | USPAT | OR | OFF | 2006/12/19 12:12 |
| L13 | 64 | 12 and simulation and block and fifo | USPAT | OR | OFF | 2006/12/19 12:17 |
| L14 | 1 | ("5548785").PN. | USPAT | OR | OFF | 2006/12/19 12:17 |
| L15 | 9 | ("5548785").URPN. | USPAT | OR | OFF | 2006/12/19 12:21 |
| S1 | 5 | US-6526495-$.DID. OR US-6513105-$.DID. OR US-6442646-$.DID. OR US-6128678-$.DID. OR US-6115760-$.DID. | USPAT | OR | OFF | 2006/12/19 11:28 |

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|---|---|---|---|---|---|---|
| L1 | 1 | ("5506809").PN. | USPAT | OR | OFF | 2006/12/19 15:44 |
| L2 | 5 | (("6,526,495") or ("6,513,105") or ("6,442,646") or ("6,128,678") or ("6,115,760")).PN. | USPAT | OR | OFF | 2006/12/19 15:45 |
| L3 | 2 | 2 and wait and size | USPAT | OR | OFF | 2006/12/19 15:45 |
| L4 | 15 | ("5568443" \| "5594702" \| "5809557" \| "5860119" \| "5933654" \| "6044225" \| "6069893" \| "6229812" \| "6278715").PN. OR ("6526495").URPN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/12/19 15:50 |
| L5 | 620 | fifo with dynamic | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/12/19 15:50 |
| L6 | 24 | 5 and deadlock and wait and automatically | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/12/19 15:56 |
| L7 | 3182 | (fifo and (variable with size)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 15:56 |
| L8 | 103 | 7 and (wait adj period) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 15:57 |
| L9 | 5 | 8 and deadlock | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/12/19 15:57 |

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Object-Oriented Simulation
by Zobrist, G. W.; Leonard, J. V.;
Hardcover, Edition: 1

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1. **Teams for tomorrow [groupware]**
Johansen, R.;
System Sciences, 1991. Proceedings of the Twenty-Fourth Annual Hawaii Inte Conference on
Volume iii, 8-11 Jan. 1991 Page(s):521 - 534 vol.3
Digital Object Identifier 10.1109/HICSS.1991.184183
AbstractPlus | Full Text: PDF(1056 KB) IEEE CNF
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2. **The target tracker detector for OPERA experiment**
Gornushkin, Y.;
Nuclear Science Symposium Conference Record, 2004 IEEE
Volume 1, 16-22 Oct. 2004 Page(s):126 - 129 Vol. 1
Digital Object Identifier 10.1109/NSSMIC.2004.1462081
AbstractPlus | Full Text: PDF(1472 KB) IEEE CNF
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3. **The OPERA spectrometer RPC system**
Paoloni, A.;
Nuclear Science Symposium Conference Record, 2004 IEEE
Volume 1, 16-22 Oct. 2004 Page(s):502 - 506 Vol. 1
Digital Object Identifier 10.1109/NSSMIC.2004.1462243
AbstractPlus | Full Text: PDF(446 KB) IEEE CNF
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4. **OPERA-an expert operations analyst for a distributed computer system**
Adler, R.M.; Heard, A.; Hosken, R.B.;
AI Systems in Government Conference, 1989., Proceedings of the Annual
27-31 March 1989 Page(s):179 - 185
Digital Object Identifier 10.1109/AISIG.1989.47323
AbstractPlus | Full Text: PDF(612 KB) IEEE CNF
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5. **Tests of OPERA RPC detectors**
Bergnoli, A.; Borsato, E.; Brugnera, R.; Buccheri, E.; Candela, A.; Carrara, E.; Corradi, G.; D'Incecco, M.; Corso, F.D.; Esposti, L.D.; Di Giovanni, A.; Di Marc Dusini, S.; Fanin, C.; Felici, G.; Gambarara, A.; Garfagnini, A.; Gorin, A.; Grian C.; Lindozzi, M.; Longhin, A.; Mastropietro, F.; Mengucci, A.; Monacelli, P.; Pa

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1. **Changes in magnetostriction of polycrystalline Tb-Dy-Fe casting alloys a**
Ono, H.; Shimada, M.; Toma, H.;
Magnetics, IEEE Transactions on
Volume 37, Issue 2, Part 2, March 2001 Page(s):1003 - 1010
Digital Object Identifier 10.1109/20.917184
AbstractPlus | References | Full Text: PDF(304 KB) IEEE JNL
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2. **Effects of rare-earth element substitution on magnetostrictive properties polycrystalline Tb-Dy-Fe alloys**
Ono, H.; Shimada, M.; Toma, H.;
Magnetics, IEEE Transactions on
Volume 38, Issue 6, Nov. 2002 Page(s):3665 - 3668
Digital Object Identifier 10.1109/TMAG.2002.804794
AbstractPlus | References | Full Text: PDF(262 KB) IEEE JNL
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3. **A robust two-antenna digital TV receiver for portable handsets with ultra-interference cancellation**
Kaneda, Y.; Toma, H.; Hori, M.; Sakaguchi, S.; Tamaki, S.; Tomoyose, T.; Mor Murakami, S.;
Consumer Electronics, 2006. ICCE '06. 2006 Digest of Technical Papers. Inter Conference on
7-11 Jan. 2006 Page(s):467 - 468
Digital Object Identifier 10.1109/ICCE.2006.1598513
AbstractPlus | Full Text: PDF(216 KB) IEEE CNF
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4. **A high-speed mobile OFDM receiver LSI with an iterative noise-reduction channel estimation**
Kamiyama, K.; Mizutani, H.; Toma, H.; Kaneda, Y.; Tamaki, S.; Dang Hai Phar
Consumer Electronics, 2005. ICCE. 2005 Digest of Technical Papers. Internati on
8-12 Jan. 2005 Page(s):193 - 194
Digital Object Identifier 10.1109/ICCE.2005.1429783
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1. **CDF level 2 trigger upgrade**
Anikeev, K.; Bogdan, M.; Demaat, R.; Fedorko, W.; Frisch, H.; Hahn, K.; Hakal Kim, Y.; Kroll, J.; Kwang, S.; Lewis, J.; Lin, C.; Liu, T.; Marjamaa, F.; Mansikka Pitkanen, S.; Reisert, B.; Rusu, V.; Sanders, H.; Shochet, M.; Stabenau, H.; Va Wilson, P.; Whiteson, D.; Wittich, P.;
Nuclear Science, IEEE Transactions on
Volume 53, Issue 2, April 2006 Page(s):653 - 658
Digital Object Identifier 10.1109/TNS.2006.871782
AbstractPlus | Full Text: PDF(952 KB) IEEE JNL
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2. **Space Shuttle UHF communications performance evaluation**
Hwu, S.U.; Loh, Y.C.; Dobbins, J.A.; Kroll, Q.D.; Sham, C.C.;
Aerospace and Electronic Systems Magazine, IEEE
Volume 20, Issue 10, Oct. 2005 Page(s):9 - 14
Digital Object Identifier 10.1109/MAES.2005.1581094
AbstractPlus | Full Text: PDF(353 KB) IEEE JNL
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3. **In-situ diagnostics of HV outdoor insulators using laser-induced fluoresc spectroscopy**
Larsson, A.; Roslund, A.; Kroll, S.; Dernfalk, A.;
Dielectrics and Electrical Insulation, IEEE Transactions on [see also Electrical Transactions on]
Volume 9, Issue 2, April 2002 Page(s):274 - 281
Digital Object Identifier 10.1109/94.993745
AbstractPlus | Full Text: PDF(751 KB) IEEE JNL
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4. **Magnetron Research at Columbia Radiation Laboratory**
Kroll, N.M.; Bernstein, M.J.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 2, Issue 3, Sep 1954 Page(s):33 - 37
AbstractPlus | Full Text: PDF(408 KB) IEEE JNL
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5. **The Application of Hypercomplex Matrix Analysis to Variable Parameter I**
Krongelb, S.; McNichol, J.J.; Kroll, N.;

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1. **On the performance analysis of dynamic channel allocation with FIFO ha in LEO-MSS**
Zhipeng Wang; Mathiopoulos, P.T.;
Communications, IEEE Transactions on
Volume 53, Issue 9, Sept. 2005 Page(s):1443 - 1446
Digital Object Identifier 10.1109/TCOMM.2005.855018
AbstractPlus | Full Text: PDF(296 KB) **IEEE JNL**
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2. **A dynamic voltage sensing FIFO suitable for multi-format video systems**
Yazawa, M.; Hosotani, S.; Imamura, Y.; Amishiro, H.; Okada, K.;
Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995
1-4 May 1995 Page(s):159 - 162
Digital Object Identifier 10.1109/CICC.1995.518158
AbstractPlus | Full Text: PDF(396 KB) **IEEE CNF**
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3. **Dynamic resource allocation for quality of service on a PON with home n**
Jongwook Jang; Park, E.K.;
Communications Magazine, IEEE
Volume 38, Issue 6, June 2000 Page(s):184 - 190
Digital Object Identifier 10.1109/35.846092
AbstractPlus | References | Full Text: PDF(56 KB) **IEEE JNL**
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4. **Implementation of expedited forwarding using dynamic hop counts base priority scheduling**
Ge Jingguo; Yang Mingchuan; Qian Hualin;
Communication Technology Proceedings, 2003. ICCT 2003. International Con
Volume 1, 9-11 April 2003 Page(s):324 - 333 vol.1
Digital Object Identifier 10.1109/ICCT.2003.1209094
AbstractPlus | Full Text: PDF(531 KB) **IEEE CNF**
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5. **High speed asynchronous structures for inter-clock domain communicat**
Chattopadhyay, A.; Zilic, Z.;
Electronics, Circuits and Systems, 2002. 9th International Conference on
Volume 2, 15-18 Sept. 2002 Page(s):517 - 520 vol.2

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Hang-Sheng Wang, Xinping Zhu, Li-Shiuan Peh, Sharad Malik

November 2002 **Proceedings of the 35th annual ACM/IEEE international symposium on Microarchitecture**

**Publisher:** IEEE Computer Society Press

Full text available: pdf(1.14 MB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

With the prevalence of server blades and systems-on-a-chip (SoCs), interconnection networks are becoming an important part of the microprocessor landscape. However, there is limited tool support available for their design. While performance simulators have been built that enable performance estimation while varying network parameters, these cover only one metric of interest in modern designs. System power consumption is increasingly becoming equally, if not more important than performance. It is ...

2 Concurrent simulation with entity life modeling: an airport simulator using Ada

Pen-Nan Lee, Ravinder R. Purumandla, William Nehman

December 1990 **Proceedings of the 22nd conference on Winter simulation**

**Publisher:** IEEE Press

Full text available: pdf(692.11 KB) Additional Information: full citation, references, index terms

3 Demonstrating the scalability of a molecular dynamics application on a Petaflop computer

George S. Almasi, C□lin Caşcaval, José G. Castaños, Monty Denneau, Wilm Donath, Maria Eleftheriou, Mark Giampapa, Howard Ho, Derek Lieber, José E. Moreira, Dennis Newns, Marc Snir, Henry S. Warren

June 2001 **Proceedings of the 15th international conference on Supercomputing**

**Publisher:** ACM Press

Full text available: pdf(392.72 KB) Additional Information: full citation, abstract, references, citings, index terms

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**Asynchronous FIFO** with **Dynamic Flag Logic**. No replacement. DW03_fifo_a_sf. **Asynchronous FIFO** with Static **Flag Logic**. Replaced with DW_fifo_s2_sf ...
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[PDF] Memory - **FIFO** Overview
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**flag logic** and an interface to common ASIC dual port RAMs. Choosing between the two ... Asymmetric I/O Synchronous (Single Clock) **FIFO** with **Dynamic Flags** ...
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64 x 36 x 2 Synchronous Bidirectional **FIFO** Memory with Bus ...
Each **FIFO** has **flags** to indicate empty and full conditions and two programmable **flags** ... Free-Running CLKA and CLKB Can Be **Asynchronous** or Coincident ...
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[Paper] PC based Heart Rate Monitor Implemented in XILINX FPGA and ...
CLBs provide the functional elements for constructing **logic** • IOBs provide the interface ... 2.3 **Asynchronous FIFO**: In this project using **Asynchronous FIFO**. ...
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Predictive status **flag** generation in a first-in first-out (**FIFO** ...
Preferably, the **asynchronous FIFO** read and write pointers are ... state machine **logic** to minimize **flag** access times and to maximize **flag dynamic** accuracy. ...
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Split **FIFO** configuration of block RAM - Patent 7106098
When configuring the **FIFO logic** for an **asynchronous FIFO** memory system in a CLB of a PLD, the **flags** are often in transition due to some of the **logic** in the ...
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[PDF] ProASIC3/E SRAM/**FIFO** Blocks
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Since the **FIFO logic** can only be used in conjunction with the memory block, ... **dynamic flags**. Static **flag** settings are configured using configuration flash ...
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OpenChoice IP Catalog : Digital
CDN-CW_fifo_s1_df, Synchronous (Single Clock) **FIFO** with **Dynamic Flags** ... CDN-CW_ram_2r_w_s_dff, Synchronous Write Port, **Asynchronous** Dual Read Port RAM ...
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[DOC] Digital Notch Filter Operating Manual
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The purpose of the digital notch filter is to decrease the total **dynamic** range ... **Fifo** Error –